

Semiconductor fundamentals

lec 04
15/7/15

Topics: Fermi-Dirac fraction

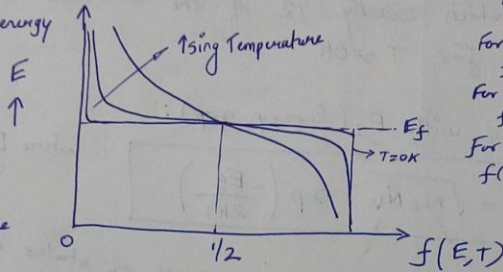
- Physical interpretation of fermi-level (E_F)
- Relation of n_i with E_g (Energy gap)
- Sources of carriers in extrinsic semiconductor
- Charge-balance equation
- Band model of extrinsic semiconductor
- Heavy doping effects on EBD
- Carrier transport
- Drift and diffusion current eq's.
- Types of Scattering
- Methods of generating excess carriers
- Injection level concept.
- Effect of excess carriers on EBD
- Concept of Quasi-Fermi level
- Direct Vs Indirect Bandgap Semiconductor
- Carrier Lifetime

• Fraction of available states occupied at any E, T is 01

$$f(E, T) = \left[\frac{1}{1 + \exp\left(\frac{E - E_F}{kT}\right)} \right] \quad \text{--- Fermi-Dirac fraction} \quad \text{--- eqn (1)}$$

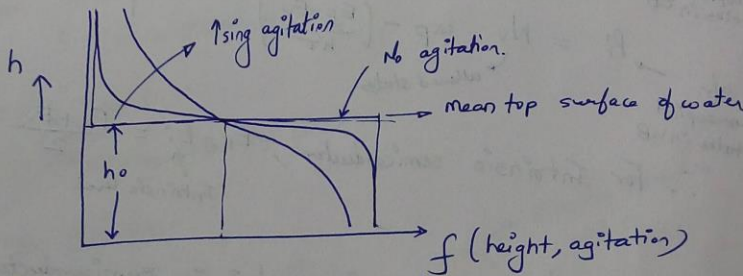
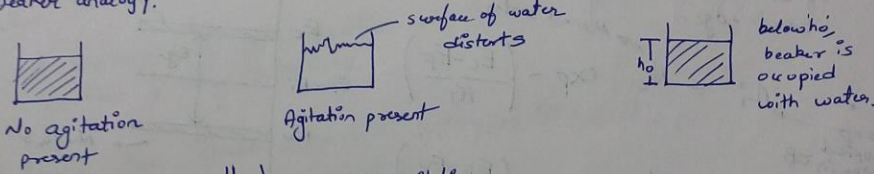
where E_F : Fermi level
 kT : Thermal energy

• Eqn (1) tells that e^- from lower energies are moving to higher energies as Temperature rises.



For $E \rightarrow \infty$
 $f(E, T) = 0$
 For $E = E_F$
 $f(E, T) = 1/2$
 For $E \rightarrow -\infty$
 $f(E, T) = 1$

To understand the shape of Fermi-Dirac function we use Water in beaker analogy.



In this analogy,

- Water in beaker \longleftrightarrow electrons
- Beaker volume \longleftrightarrow allowed energies
- height \longleftrightarrow energy
- agitation \longleftrightarrow Temperature
- Fraction of beaker volume \longleftrightarrow Fraction of occupied states
- mean top surface \longleftrightarrow Fermi level.

fraction of beaker volume occupied.

Physical significance of Fermi-level (E_F):

It is that

- (i) Energy below (above) which all allowed levels are occupied (unoccupied) at $T=0K$
- (ii) Energy at which exactly $1/2$ of the available states are occupied for $T > 0K$

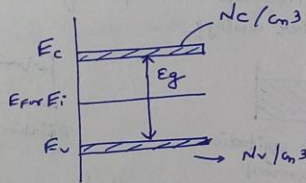
Relation of n_i with E_g (Energy gap):

Intrinsic e^- conc $n_i = \sqrt{N_c N_v} \exp\left(\frac{-E_g}{2KT}\right)$ where $E_g = E_c - E_v$

Effective density of states at conduction (CB) band edge. Effective density of states at valence band edge.

no of occupied states in CB $n_i = N_c \exp\left(\frac{-(E_c - E_F)}{KT}\right)$ Allowed states

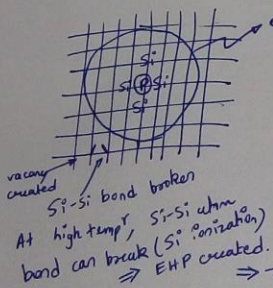
no of unoccupied states in VB $p_i = N_v \exp\left(\frac{-(E_F - E_v)}{KT}\right)$ Allowed states



\therefore For Intrinsic semiconductor, $E_F = E_i = \frac{E_c + E_v}{2}$
Intrinsic level

Sources of carriers in extrinsic semiconductor:

To understand this, let consider Phosphorus doped Si (ie N type Si) \rightarrow has 5 valence e^- .



At $T > 0K$:
 \rightarrow This e^- becomes free bcoz of thermal vibration of Si atoms.
The energy required to free this 5th e^- from the orbit is ionization energy.

$E_{ion}(Ph) = 0.045 eV$ \rightarrow (this is very small)

\therefore Even at $T > 0K$ (lower temp), most of impurity atom (Ph) get ionized & each Ph atom will contribute to an e^- which is available for conduction, but no hole is created \Rightarrow This is addⁿ source of carrier.

∴ Sources of Carriers in extrinsic semiconductor: 03

Process	Result	Energy required (For Si)
Impurity ionization	only e ⁻ or only hole	0.045eV
Silicon ionization or (Thermal generation)	EHP (Electron-hole pair)	1.1eV ↳ This much energy is required to break Si-Si bond

$$\frac{N_d^+}{N_d} \leq 1$$

↑
 conc of ionized P atoms
 ↑
 conc of P atoms (donor)

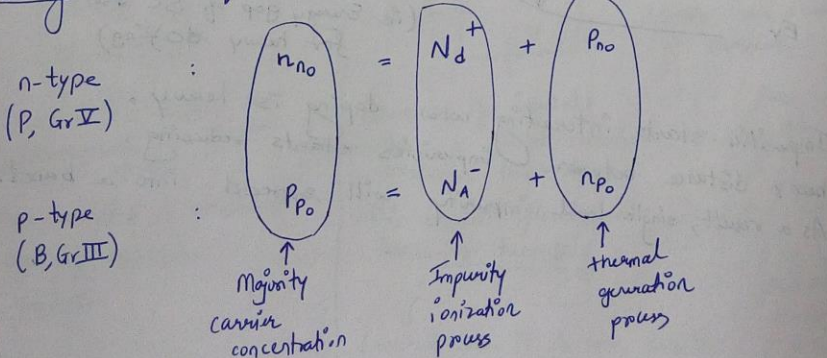
At room temp, most of impurities are ionized, because energy req^d for this is only 0.045eV.

Similarly, for Boron doped Si (P-type Si):
 $E_{ion}(B) = 0.045eV$

$$\frac{N_a^-}{N_a} \leq 1$$

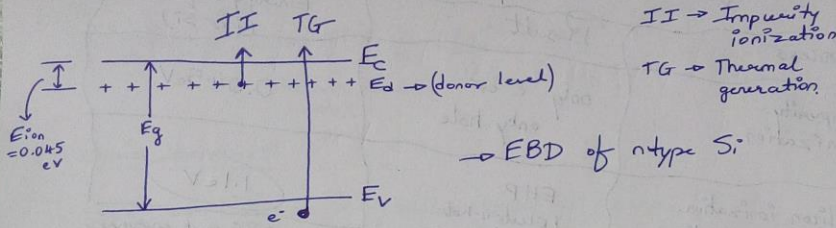
↑
 conc of ionized B atoms
 ↑
 acceptor impurity concⁿ

• Charge balance equation:



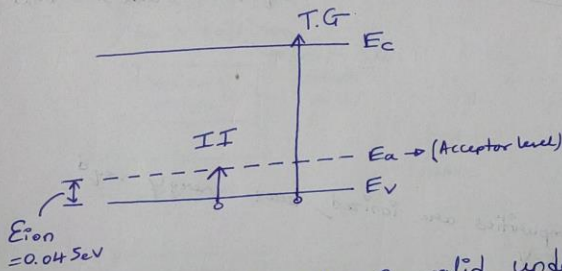
• Band model of extrinsic semiconductor

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II → Impurity ionization
TG → Thermal generation

→ EBD of n-type Si

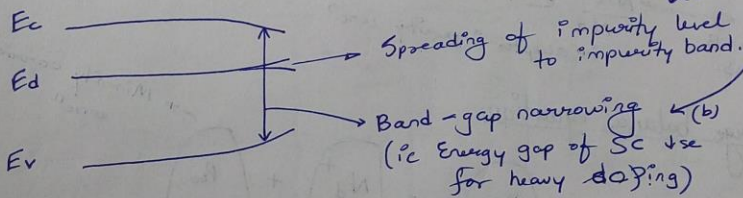


→ EBD of p-type Si

→ The above EBD picture is valid under Moderate doping.

• Heavy doping effects on EBD:

Two effects of heavy doping



• Impurities starts interacting when doping is heavy, because distance between impurities starts reducing. As a result, single level impurity will spread into a band.

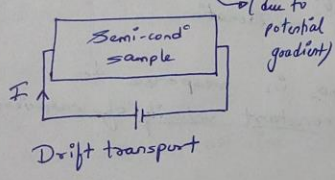
$p_{n0} n_{n0} = N_c N_v \exp\left(-\frac{E_g}{kT}\right) = n_i^2$ --- Law of mass action

For n-type semiconductor: $p_{n0} = \frac{n_i^2}{n_{n0}} = \frac{n_i^2}{N_d}$ ($n_{n0} = N_d$)
minority carrier conc.

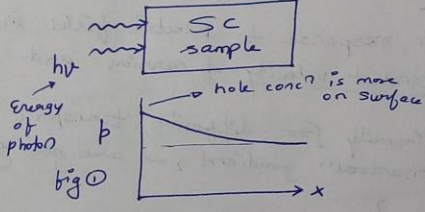
Carrier transport:

Let's see some examples of various modes of transport

1. Flow of current in SC is due to 'drift' here



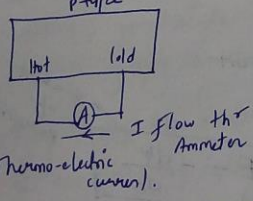
2. Light falling on SC sample



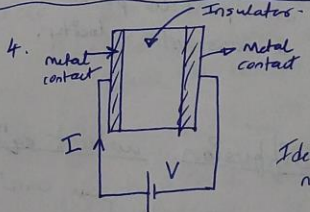
If $h\nu > E_g$ such that EHP's are created. Light (photon) is being absorbed within a short distance from surface, excess/extra carriers will be created near the surface.
 ∴ Conc. of carriers near the surface will be higher than in the bulk. Thus conc. gradient of carriers is established (shown in fig 0).

Diffusion transport
 • Here hole current is due to conc. gradient (i.e. due to diffusion)
 • Diffusion transport

3. Sample with two contacts (i.e. Hot & cold contacts)



Here, current I is established becoz of temperature gradient.



However, if insulator is vthin (few nm), then we find current flowing in response to voltage.
 This current flowing thr insulator is becoz of tunneling. (It is a Quantum mechanics phenomenon)
 [we will cover it in some details later!]

- Einstein relation (It shows that both drift & diffusion are based on random thermal motion) 06

Diffusivity = Mobility \times Thermal voltage

constant associated with diffusion transport

$$D = \mu V_t$$

constant associated with drift transport

$$V_t = \frac{kT}{q}$$

holds under quasi-equilibrium.

Facts:

- 1) In response to Electric field in drift transport, we are getting constant velocity of carriers and not acceleration
- 2) Similarly for diffusion transport also, in response to concentration gradient, we are getting constant velocity of carriers.

Why?

In semiconductor, we have other particles surrounding the e^- , & e^- is being scattered, that is why it is undergoing random thermal motion.

It is because of this random motion that, e^- is not able to continuously accelerate (as e^- is encountering friction during its moment because of other particles present in crystal). Thus, it can only acquire a constant velocity.

Drift and Diffusion current eqⁿs.

in p-type SC

$$J_p^{diff} = -q D_p \left(\frac{dp}{dx} \right)$$

$$J_p^{drift} = -q p \mu_p \frac{d\psi}{dx}$$

$$J_p^{drift} = q p \mu_p E$$

concⁿ gradient

where

$E = -\frac{d\psi}{dx}$

gradient of potential

E-field

* Types of scattering:

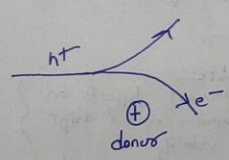
- 1. Ionized impurity scattering. (for doped semiconductors)
- 2. Phonon/Lattice scattering. (e^- colliding with phonons)
- 3. Carrier-carrier scattering. (involves e^- & holes)

vibrating Si atoms or lattice site.

As Temp ↑, LS ↑scs
(Lattice scattering) → (Physical collision betⁿ carriers & lattice site.)

- phonons
- Ionized impurities (II scattering) → (It is becoz of action at a distance, it need not involve a physical collision)
- electrons } (Carrier-carrier scattering)
- holes }
- In scattering, direction of motion of particle which has encountered a 'collision' cannot be predicted exactly.

Ionized impurity scattering



- II scattering changes directⁿ of particle.
- It takes place becoz of force of attractⁿ or repulsion betⁿ particle & this charge.

- More the number of ionized impurities, greater is the chance of carriers getting scattered.
- For rising temp, the scattering events will be less (becoz of ionized impurities)
- So, higher the concⁿ of impurities, higher is the chances of scattering
- ie II scattering is more at lower temperatures.

Carrier-carrier Scattering (CCS)

- e^- s by e^- s
 - e^- s by ht
 - ht by e^-
 - ht by ht
- 4 events in CCS

- CC scattering is not important for carriers of same polarity.
- But c-c scattering by carriers of opposite polarity ⇒ definitely affects the current.

Let us see how?

If a e^- /hole is colliding with other e^- /hole, it will transfer its momentum & energy to other e^- .

Now, current is not affected, since current is result of (energy & momentum of entire population) in any directⁿ.

For current to flow, it should be the directed component of momentum.

- Carrier-carrier scattering affects only the minority carriers mobility in a semiconductor. (expect at v. high doping levels)
CC scattering can affect the mobility of both e^- & holes

• Conductivity (σ)

$$\sigma = qn\mu_n + qp\mu_p$$

For n-type semiconductor:

$$\sigma = qn_0\mu_n + qp_0\mu_p \quad (\because p_0 \ll n_0) \text{ in n-type}$$

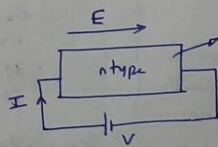
$$\sigma \approx qn_0\mu_n \quad (\text{This means that only majority carriers conc. decides the conductivity})$$

• Methods of generating excess carriers:-

1. Impact ionization
2. Photo-ionization
3. Injection mechanism
4. Band to band tunneling

1. Impact ionization

• Excess carriers are generated under the influence of high E-field by Impact ionization.



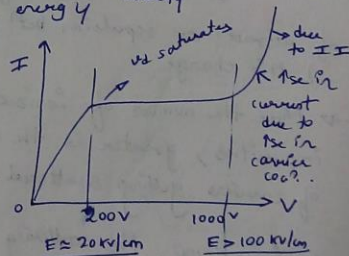
Assume semiconductor sample to be uniform.

→ Betⁿ any 2 collision's, carriers are gaining energy from the E-field.

if this energy ie gained is \gg large, that the carriers when it collides with Si atom, it can break a bond & can generate excess EHP.

So, this is the situation of Impact ionization.

Note: current depends on carrier conc. & drift velocity

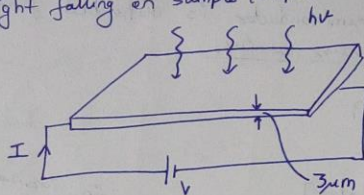


• Excess carriers: means carriers over & above the equilibrium value.

Note: Generally, excess carrier are created in pairs.

2. Photo-ionization / generation:-

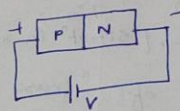
eg Light falling on sample (n-type)



Here, each photon having energy $hv > E_g$ can participate in breaking of bi bond & creating an excess EHP.

3. Injection mechanism:

Consider a p-n junction



Forward junction biased.

Here, p-region will inject extra holes (e^-) into n-region.

Thus, excess e^- & holes are created by applⁿ of Voltage.

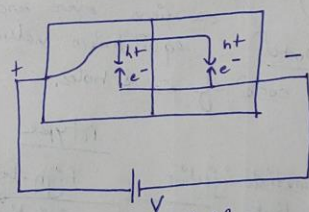
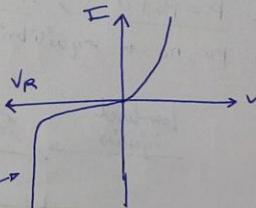


fig: Process of injection

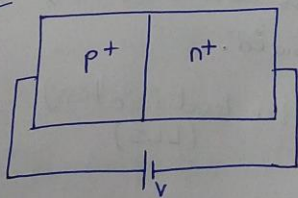
Unique situation in SC
↳ Neutralize means 'zero change'!



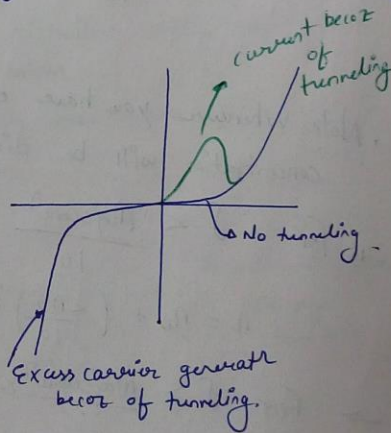
For this reverse vty, a high E-field is created near the Jⁿ (junction), which may give rise to excess EHP becoz of impact ionization or 'Tunneling'.

4. Band-to-band tunneling:

This eg shows a heavily doped p & n regions



We will explore this type later while encountering a diode.



• Injection level concept:

refer to the extent to which semi-conductor is disturbed from equilibrium becoz of excess carriers.

• Let's consider charge-balance eqⁿ:

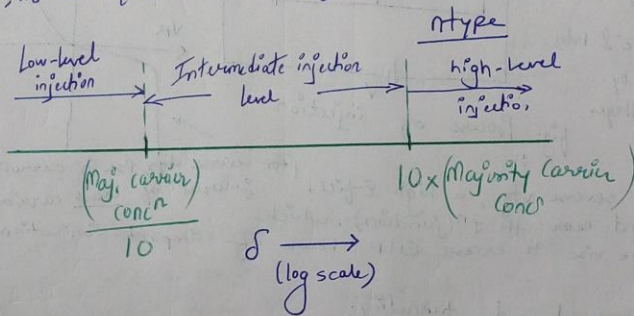
$$\delta [p + N_d^+ - n - N_a^-] = 0$$

ie $\delta p - \delta n = \delta [N_a^- - N_d^+] \approx 0$
 This is (situation in most sc)
 If change in ionizati of impurities is negligible then $\delta p \approx \delta n$

This is the meaning of "excess carriers are generated in pairs!"

$\delta p = \delta n = \delta$ symbol represents excess carriers over and above the equilibrium value.

ie $p = p_0 + \delta$, $n = n_0 + \delta$
 where $p_0, n_0 \rightarrow$ equilibrium concⁿ of e^s & holes.



• Note: Whenever you have excess carrier generation, minority carrier concentration will be distributed.

→ For $\delta < \frac{\text{Maj conc}^n}{10}$ (Low level injection) (LLI)

$$n = n_0 + \left(\frac{< n_0}{10} \right) \approx n_0$$

→ For $\delta > \frac{\text{Maj conc}^n}{10} \times 10$ (High level injection) (HLI)

$$n = n_0 + \left(\frac{> 10 n_0}{\delta} \right) \approx \delta$$

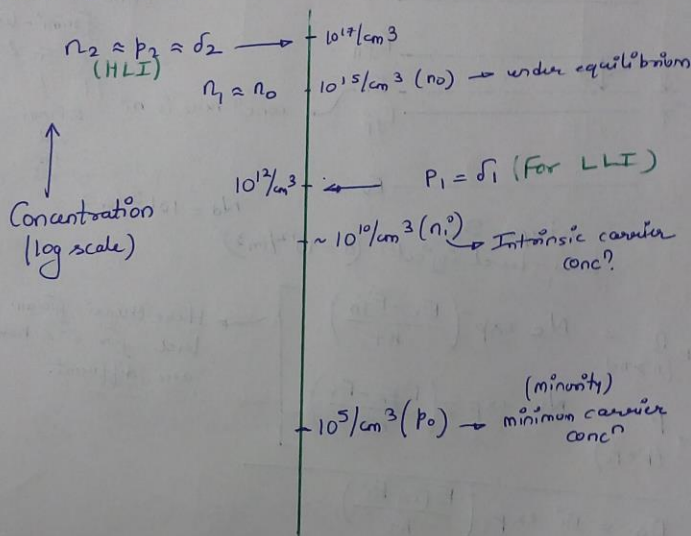
$$p = p_0 + \delta \approx \delta$$

Let us take an example to understand LLI & HLI:

n-type, $N_d = 10^5/cm^3$, Phosphorous doped Si^0

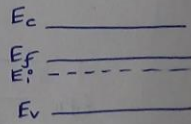
LLI: $\delta_1 = 10^{12}/cm^3$

HLI: $\delta_2 = 10^{17}/cm^3$



Inferences from above discussion are:

- 1) For LLI, minority carrier concⁿ is disturbed, while maj. carrier concⁿ is almost same as in equilibrium.
- 2) For HLI, both majority and minority carrier concⁿ are almost same.



Effect of excess-carriers on EBD:

$$n_0 = N_c \exp\left(-\frac{E_c - E_f}{kT}\right)$$

$$p_0 = N_v \exp\left(-\frac{E_f - E_v}{kT}\right)$$

Both n_0 & p_0 are characterized by same Fermi level EBD of n-type SC

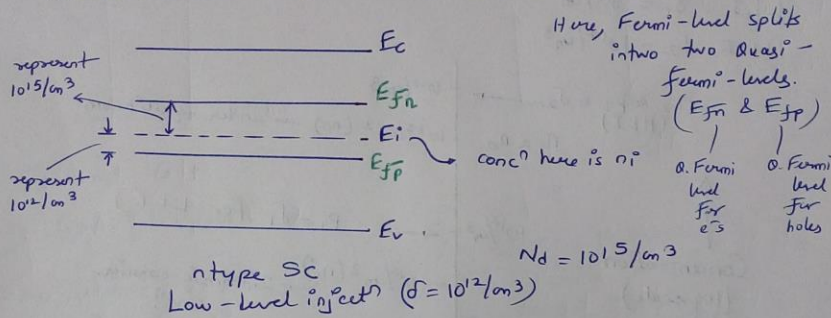
$$n_0 = n_i \exp\left(\frac{E_f - E_i}{kT}\right)$$

$$p_0 = n_i \exp\left(\frac{E_i - E_f}{kT}\right)$$

n_0 & p_0 in terms of derived from intrinsic SC

The effect of excess carriers on EBD is different. Quasi-Fermi ¹²
 levels for e^- & holes:

• Quasi-Fermi level (under LLI and HLI):



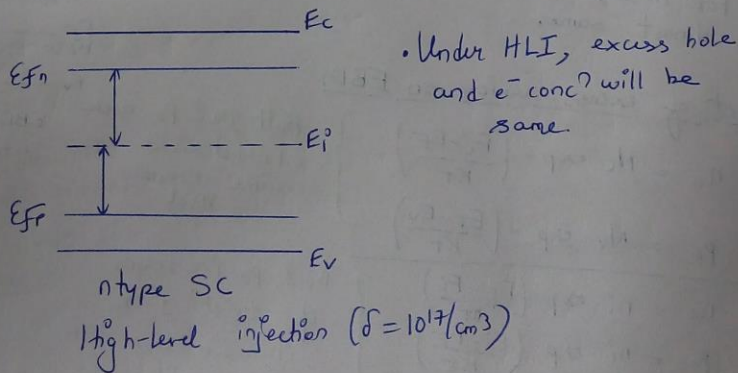
$$\left. \begin{aligned} n &= N_c \exp\left(-\frac{E_c - E_{fn}}{kT}\right) \\ p &= N_v \exp\left(-\frac{E_{fp} - E_v}{kT}\right) \end{aligned} \right\} \begin{array}{l} \text{concn of } e^- \\ \text{concn of holes} \end{array}$$

Here Quasi-Fermi level for e^- & holes are different.

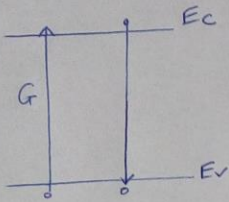
$$n_0 = n_i \exp\left(\frac{E_{fn} - E_i}{kT}\right)$$

$$p_0 = n_i \exp\left(\frac{E_i - E_{fp}}{kT}\right)$$

Note: Quasi-Fermi level are for ^(Quasi) Non-equilibrium conditions.



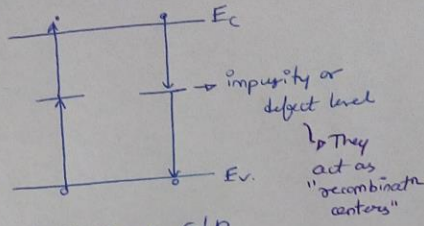
Mechanism's of Generation-recombination phenomena:-



Direct G/R

eg GaAs (Compound SC)

• Here, whenever an e^- meet a h^+ , a Recombⁿ occurs.

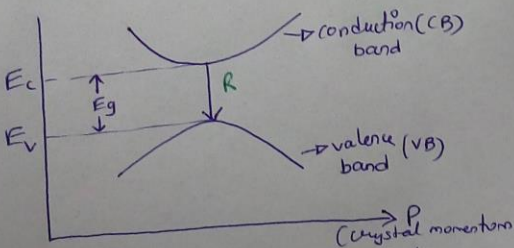


Indirect G/R

eg Si

• In Si, 'R' occur via impurity sites within the energy gap.
(So, near certain defect/impurity level within a SC, if e^- & h^+ meet, R can occur)

Direct Vs Indirect Bandgap Semiconductors:-

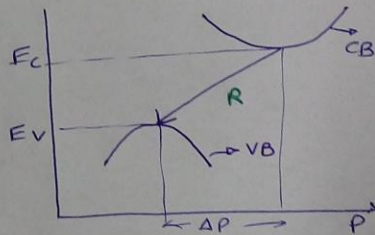


Direct band gap semiconductor

eg GaAs

• Here, momentum change is zero for the transition. & thus can be given to a photon
→ hence suited for opto-electronic devices

• Here, recombⁿ process is radiative (direct)
• Uses for making LEDs



Indirect band gap SC

eg Si.

• Here during transition (e^- from CB to VB), there will be a change in momentum (ΔP).

• Here, recombⁿ process is non-radiative.

• That means we cannot use Si for emitting light.

• Here, momentum can only be dissipated to a phonon or e^- .