

Forward bias showing applied built-in field.

Numericals: (pn junction under zero applied bias)

1. An abrupt and uniformly doped pn junction has doping levels  $N_a = 10^{16} \text{ cm}^{-3}$ ,  $N_d = 10^{15} \text{ cm}^{-3}$  at 300K.

$$[n_i = 1.5 \times 10^{10} \text{ cm}^{-3}, \epsilon_{si} = \epsilon_0 \times 11.7 = 8.85 \times 10^{-14} \times 11.7 \approx 10^{-12}]$$

Estimate a) Built-in potential.

b) Space charge width

c) Peak Electric field.

sol<sup>n</sup>: 1) For abrupt pn junction,

$$V_{bi} = \frac{kT}{q} \ln \left[ \frac{N_a N_d}{n_i^2} \right] \quad ; \quad V_T = \frac{0.0259 \text{ V}}{26 \text{ mV}}$$

$$= V_T \ln \left[ \frac{10^{16} \times 10^{15}}{(1.5 \times 10^{10})^2} \right] = 0.0259 \ln \left[ \frac{10^{16+15-20}}{2.25} \right]$$

$$V_{bi} = 0.635 \text{ V} \rightarrow \text{Built-in voltage.}$$

2) Space charge width (w):

$$W = \sqrt{\frac{2 \epsilon_{si}}{q} V_{bi} \left[ \frac{N_a + N_d}{N_a N_d} \right]}$$

$$W = \sqrt{\frac{2 \times 0.635 \times 10^{-12} \times \left[ \frac{10^{16} + 10^{15}}{10^{16+15}} \right]}{1.6 \times 10^{-19}}}$$

$$W = \sqrt{\frac{2 \times 0.635 \times 1.1 \times 10^{16-12+19-16-15}}{1.6}} = \sqrt{\frac{2.2 \times 0.635 \times 10^{-8}}{1.6}}$$

$$W = 0.934 \times 10^{-4} \text{ cm}$$

$$W \approx 0.934 \mu\text{m} \rightarrow \text{Space charge width}$$

$$3) E_m = -\frac{2V_{bi}}{W} = -\frac{2 \times 0.635}{0.934 \times 10^{-4}} = -13.597 \times 10^3 \text{ V/cm}$$

$$E_m = -13.6 \frac{\text{KV}}{\text{cm}} \rightarrow \text{Peak Electric field}$$

Forward voltage (V)  
 $V_0 \rightarrow$  applied F.B. voltage.  
 or forward bias showing applied built-in field.

2. Calculate  $V_{bi}$ ,  $W$ ,  $x_n$ ,  $x_p$ ,  $E_m$  for a Si abrupt pn junction at 300K with  $N_a = 10^{18} \text{ cm}^{-3}$ ,  $N_d = 10^{15} \text{ cm}^{-3}$   
 $[n_i = 1.5 \times 10^{10} \text{ cm}^{-3}, \epsilon_s = 8.85 \times 10^{-14} \times 11.7]$

Sol<sup>n</sup>: i)  $V_{bi} = V_T \ln \left[ \frac{N_a N_d}{n_i^2} \right] = 0.0259 \times \ln \left[ \frac{10^{18} \times 10^{15}}{(1.5 \times 10^{10})^2} \right] = 0.754 \text{ V}$

ii)  $W = \sqrt{\frac{2\epsilon_s V_{bi}}{q} \left[ \frac{N_a + N_d}{N_a N_d} \right]} = \sqrt{\frac{2 \times 11.7 \times 8.854 \times 10^{-14} \times 0.754}{1.6 \times 10^{-19}} \left[ \frac{10^{18} + 10^{15}}{10^{18} \times 10^{15}} \right]}$

$W = 9.88 \times 10^{-5} \text{ cm} \approx 0.988 \mu\text{m}$

iii)  $|x_n| = \left( \frac{N_a}{N_a + N_d} \right) W = 98.70 \times 10^{-6} \text{ cm}$

$|x_p| = \left( \frac{N_d}{N_a + N_d} \right) W = 9.87 \times 10^{-8} \text{ cm}$

iv)  $E_m = -\frac{2V_{bi}}{W} = -15.26 \times 10^3 \text{ V/cm}$

3. An abrupt Si pn junction at zero bias is doped uniformly with  $10^{16} \text{ cm}^{-3}$  atoms of Boron on p-side and  $10^{15} \text{ cm}^{-3}$  atoms of Phosphorus on n-side. at  $T=300\text{K}$ .

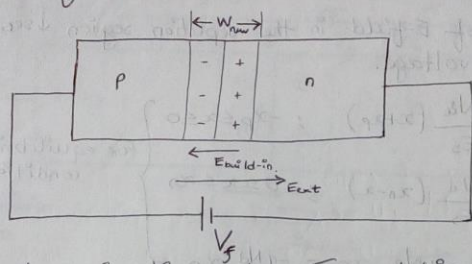
a) Calculate the Fermi level on each side of the junction w.r.t intrinsic Fermi level.

b) Sketch the equilibrium energy-band diagram for the junction and determine  $V_{bi}$  from the diagram and the results of part (a).

c) Calculate  $V_{bi}$ , and compare the results to part (b).

d) Determine  $x_n$ ,  $x_p$  and the peak electric field for this junction.

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$E_{build-in}$  → Built-in E-field  
 $E_{ext}$  → Externally applied E-field due to applied forward voltage ( $V$ )  
 $V_F$  → applied F.B voltage.

Fig 1: A pn junction under forward bias showing applied E-field and internal built-in field.

- Under forward-bias, a pn junction is not in thermal equilibrium.
  - Depletion region width reduces. ( $W \downarrow$ )
  - Equilibrium Fermi energy level changes.
  - Net Electric field reduces (since applied E-field opposes the built-in E field).

The Energy bands at forward bias condition shift due to applied voltage  $\Rightarrow$  The Fermi level on n side  $E_{Fn}$  moves upwards and above  $E_{Fp}$  due to energy of applied voltage.

$\Rightarrow \therefore$  Total barrier potential reduces :-

$$V_{total} = V_{bi} - V_F \quad \text{--- (1)}$$

Space charge width ( $w$ ) reduces with  $\uparrow$  in  $V_F$  :-

$$W_{nw} = \sqrt{\frac{2\epsilon_s (V_{bi} - V_F)}{q} \left[ \frac{N_A + N_D}{N_A N_D} \right]} \quad \text{--- (2)}$$

Why?

$\hookrightarrow$  As net E-field is lowered below thermal equilibrium value due to applied F.B  $V_F$ ,  $\Rightarrow$  thus depletion space charge width ( $w$ ) decreases.  $\therefore W \downarrow$  with an  $\uparrow$  in applied forward bias  $V_F$

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• Electric field for pn junction under forward bias →

• The magnitude of E-field in the depletion region ↓ with an applied forward bias voltage.

The equations:  $E = \frac{-qNa}{\epsilon_s} (x+x_p) \quad ; \quad -x_p \leq x \leq 0$

2)  $E = \frac{-qNd}{\epsilon_s} (x_n-x) \quad ; \quad 0 \leq x \leq x_n$

3)  $E_{max} = \frac{-qNa}{\epsilon_s} \cdot x_p = \frac{-qNd}{\epsilon_s} x_n$

} For equilibrium conditions

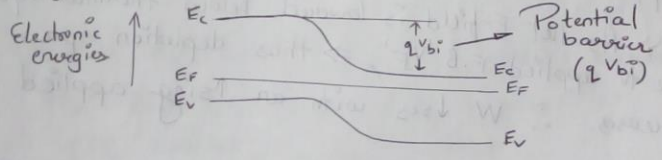
Equations (1),(2),(3) are valid and are linear functions of distance through space-charge region.

• Since  $x_n$  and  $x_p$  reduce with forward bias voltage and Maximum E-field due to applied F.B occurs at junction (ie  $x=0$ )

$$\therefore E_m = \frac{-2}{w} (V_{bi} - V_F) \quad \text{--- (3)}$$

Note (R):

• When external bias is applied to the junction, the potential barrier is raised or lowered from the value of the built-in or contact potential, and the Fermi levels on either side of the junction are shifted w.r.t to each other by an energy  $qV$  numerically equal to the applied voltage in volts.

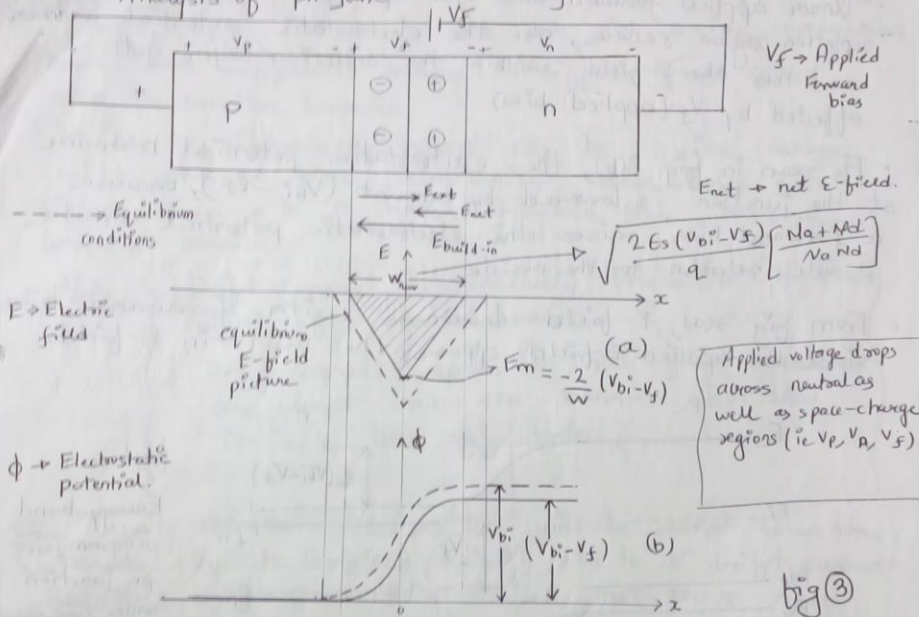


EBD of pn junction under equilibrium.

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Analysis of pn junction under forward bias contd...

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Analysis steps:

- Assumptions:
- 1) Boundary between space-charge and neutral regions is abrupt.
  - 2) pn junction is abrupt
  - 3) p and n regions are uniformly doped.

Additional assumptions:

- 1) Applied bias is small (ie quasi-equilibrium approximation)
- 2) p and n region  $\rightarrow$  Quasi-neutral regions
- 3) Entire applied voltage ( $V_f$ ) drops across the Depletion/space-charge region

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(ie  $v_p \& v_n \ll V_{bi}$  so that

$$V_{bi} \approx V_f (\text{applied voltage})$$

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- Under applied forward bias for pn junction, width of depletion region reduces, also the electrostatic potential barrier and thus the E-field within the depletion layer gets affected by  $V_f$  (applied bias).
- As seen in fig 3(b), the electrostatic potential barrier at the junction is lowered by amount  $(V_{bi} - V_f)$ , because a forward bias raises the electrostatic potential on the p-side relative to the n-side.
- From fig 3(a), E-field decreases with forward bias since the applied E-field opposes the built-in E-field.

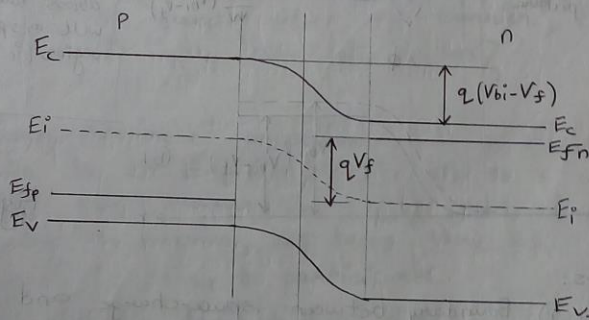


fig 4:  
Energy-band diagram for pn junction under forward bias.

- The separation of energy bands is a direct function of the electrostatic potential barrier at the junction.
- The height of electron energy barrier is  $q(V_{bi} - V_f)$  for forward bias conditions. Thus the energy bands are separated less  $[q(V_{bi} - V_f)]$  under forward bias than at equilibrium.
- Under forward bias, the Fermi level on the n-side  $E_{Fn}$  is above  $E_{Fp}$  by energy ' $qV_f$ '.

NOTE: On EBD, we show electronic energies going up, since more negative regions will be shown with higher energies, i.e. why  $E_{Fn}$  is above  $E_{Fp}$

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Under forward bias, the potential barrier is lowered<sup>05</sup> to  $(V_{bi} - V_s)$ , and many more  $e^-$ s in n-side conduction band have sufficient energy to diffuse from n to p over the smaller barrier.

∴ The  $e^-$  diffusion current can be quite large with forward bias. Similarly, more holes can diffuse from p to n under forward bias becoz of lowered barrier.

Note: { Drift current is relatively insensitive to the height of potential barrier }  
{ Drift current implies → how fast carriers are swept down the barrier, but also it implies how often }

eg: Minority carriers  $e^-$  on p-side will be swept down the barrier by the E-field, giving rise to  $e^-$  drift current. But this drift current is small not becoz of the size of the barrier, but becoz there are very few minority  $e^-$ s in p-side to participate.

∴ Diffusion currents dominate over drift in a forward bias.

Under forward bias in summary, there is smaller potential barrier, reduced E-field, diffusion of majority carriers and drift due to minority carriers.

Note: The injection of holes into the n-region means that these holes are minority carriers.  
Similarly, the injection of  $e^-$ s into p-region means that these  $e^-$  are minority carriers.

Analysis of pn junction under reverse-bias 06

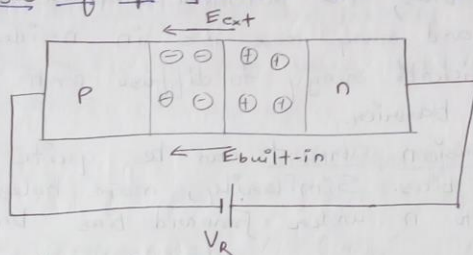


Fig 5: A pn junction under reverse bias showing applied E-field and internal built-in E-field.

- Under Reverse bias
- 1) pn junction - no longer in equilibrium
  - 2) Depletion region width increases ( $w \uparrow$ )
  - 3) Equilibrium Fermi energy level changes
  - 4) Net Electric field increases

- Effect on Fermi levels  $\Rightarrow$  Fermi level on the p-side  $E_{fp}$  is above the Fermi-level on the n-side i.e.  $E_{fn}$ .
- The difference between the two Fermi-levels is equal to the applied reverse voltage in units of energy.

$\Rightarrow \therefore$  Total potential barrier :-

$$V_{total} = V_{bi} + V_R \quad \text{--- (1)}$$

where  $V_{bi} \rightarrow$  Built-in potential in thermal equilibrium.

$$\frac{kT}{q} \ln \left( \frac{N_a N_d}{n_i^2} \right)$$



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- Space-charge width ( $w$ ) and E-field under Reverse Bias:-
- Magnitude of E-field rises above equilibrium value for applied reverse bias voltage.
- E-field rises  $\Rightarrow$  that the number of  $+$ ve &  $-$ ve charges rise if E-field rises, and this is possible if  $w$  rises.
- $\therefore$  Space-charge width  $w$  rises, with an rising  $V_a$ .

$$W_{nw} = \sqrt{\frac{2\epsilon_s (V_{bi} + V_a)}{q} \left[ \frac{N_a + N_d}{N_a N_d} \right]} \quad \text{--- (2)}$$

Also, the maximum E-field for a pn junction under reverse-bias is

$$E_m = -\frac{2(V_{bi} + V_a)}{w} \quad \text{--- (3)}$$

Note: (Rd)

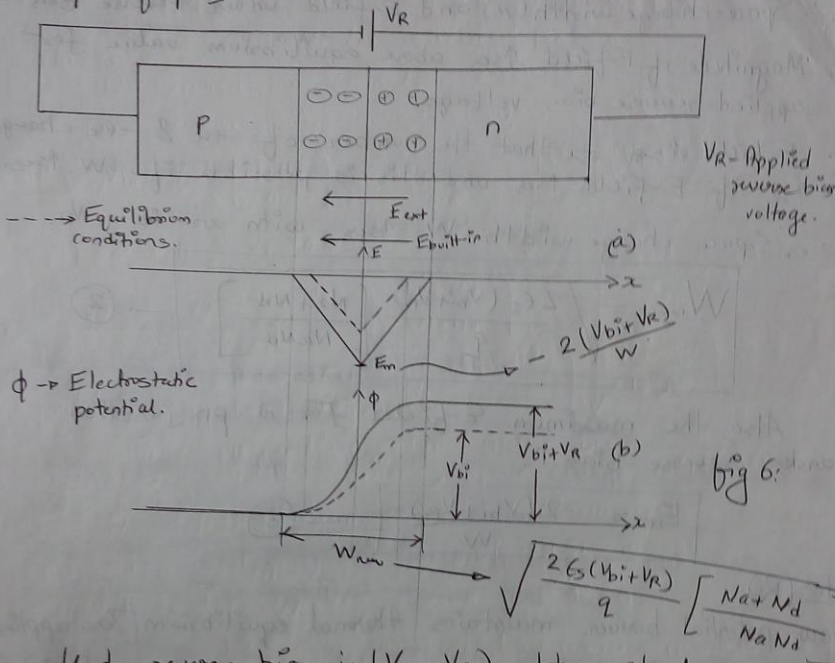
How potential barrier maintains thermal equilibrium (Zero applied bias)

- The potential barrier seen by the  $e^-$ s, for example, holds back the large concentration of  $e^-$ s in the n-region and keeps them from flowing into the p-region.

- Similarly, the potential barrier seen by the holes holds back the large concentration of holes in the p-region and keeps them from flowing into the n-region.

Thus, potential barrier in a pn junction under zero applied bias maintains thermal equilibrium

Analysis of pn junction under reverse bias contd --- 08.



$V_R$  - Applied reverse bias voltage.

--- Equilibrium conditions.

$\phi$  -> Electrostatic potential.

fig 6:

• Under reverse bias i.e. ( $V = -V_R$ ) the electrostatic potential of the p-side is depressed relative to the n-side and the potential barrier at the junction becomes larger ( $V_{bi} + V_R$ ). (from 6(b))

• With reverse bias, the Electric field at the junction is increased by the applied field, which is in the same direction as the equilibrium field. (from 6(a))

• Under reverse bias, the energy bands are separated by more than  $q(V_{bi} + V_R)$ , whereas  $E_{sp}$  is  $qV_R$  ~~less~~   
 joules higher than  $E_{fn}$ .

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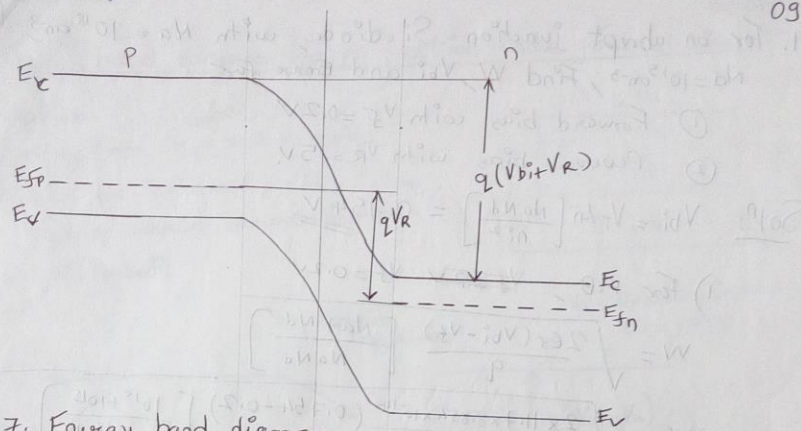


Fig 7: Energy band diagram for a p-n junction under reverse bias.

- Under reverse bias conditions, space-charge width  $W$  increases, also the <sup>total</sup> potential barrier has increased.
- Thus, the increased potential barrier means that the E-field in the depletion region has increased.
- For reverse bias, the potential barrier becomes so large ( $q(V_{bi} + V_R)$ ) that virtually no  $e^-$  in the n-side CB or holes in the p-side V.B have enough energy to surmount it.

Therefore, the diffusion current is usually negligible for reverse bias p-n junction.

- The only current which flows is due to drift mechanism of minority charge carriers.

So, under Reverse bias, drift current dominates diffusion current.

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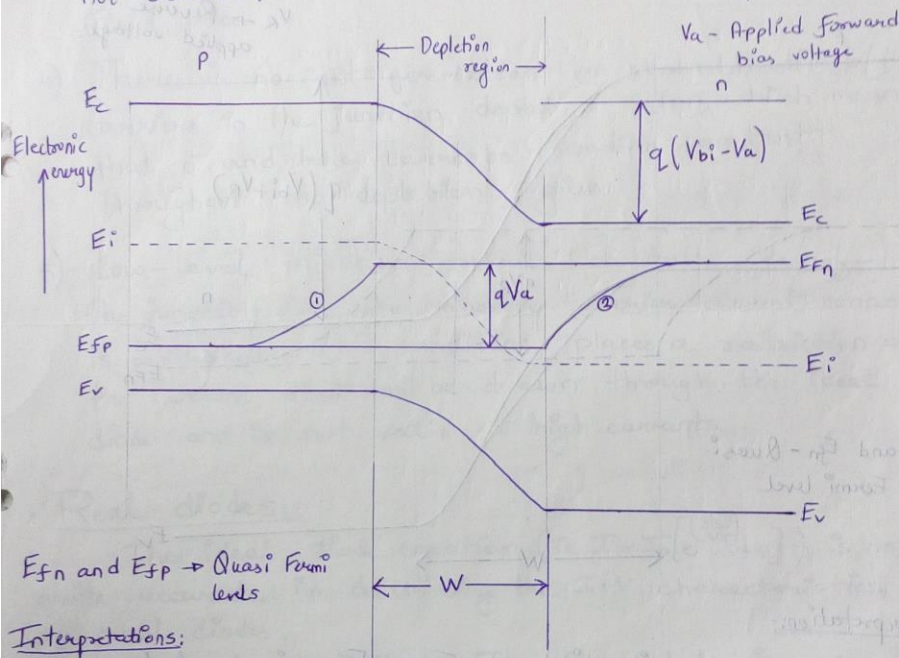
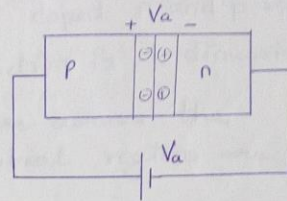
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Energy band diagram of pn junction under forward bias:-

Assumptions:

- 1) No voltage drops in <sup>neutral</sup> p and n regions
- 2) Injection level in n and p sides is low, i.e. (majority carrier concentration is not disturbed).



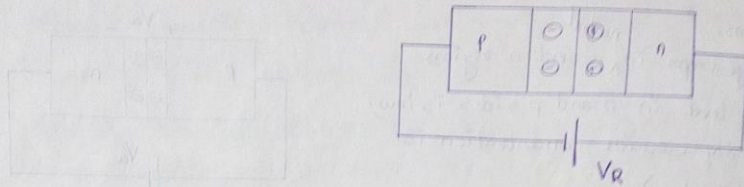
$E_{fn}$  and  $E_{fp}$  → Quasi Fermi levels

Interpretations:

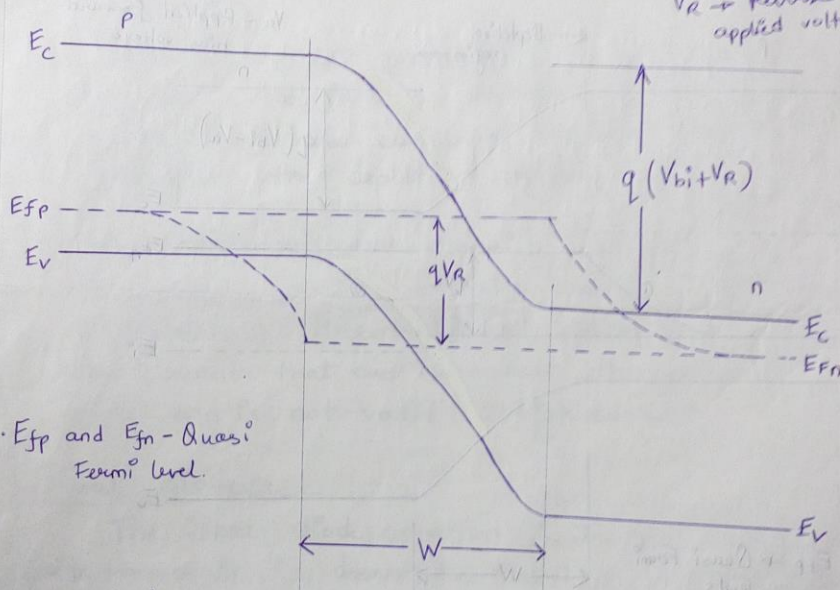
- ① and ② ⇒ Indicates the presence of excess carriers
- $E_{fn} - E_{fp} = qV_a$
- $E_{fn}$  and  $E_{fp}$  remains constant in the depletion region.
- Splitting of quasi Fermi level in the neutral n and p regions indicates the presence of excess carriers in these regions.

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Energy band diagram of a pn junction under Reverse Bias <sup>02</sup>



$V_R \rightarrow$  Reverse applied voltage.



$E_{Fp}$  and  $E_{Fn}$  - Quasi Fermi level.

Interpretation:

- $E_{Fn}$  in n-region is displaced from  $E_{Fp}$  in neutral p-region by  $qV_R$ .
- $E_{Fn}$  and  $E_{Fp}$  are constant throughout the depletion region.
- Splitting of quasi-Fermi level in the neutral n and p regions indicates the extraction of minority carriers from these regions.