

## • Forward-bias Recombination current:-

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- In forward bias of a pn junction, e<sup>-</sup>s and holes are injected across the depletion region (which further diffuse in these regions and contribute to minority current i.e. we have some excess carriers in depletion region).
- Some of these excess carriers (e<sup>-</sup>s & holes) can recombine within depletion region and not become part of minority carrier distribution & hence the current.

The Recombination rate is given by

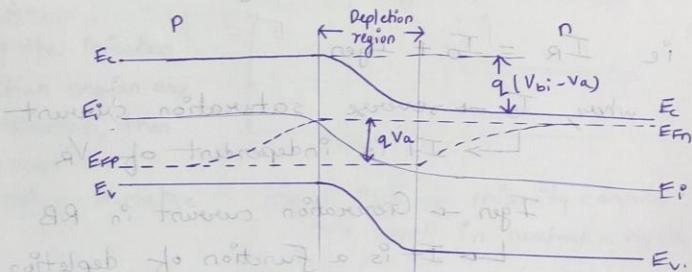
$$R = \frac{np - n_i^2}{T_{po}(n+n_t) + T_{no}(p+p_t)} \quad \text{(1)}$$

CB-conduction band  
VB-valence band

where, n, p : e<sup>-</sup>, hole concentration

$n_t, p_t$  : e<sup>-</sup>, hole concentration existing in CB(VB) with top energy level E<sub>t</sub> coincides with E<sub>Fi</sub> (Fermi level) for F.B.

V<sub>a</sub> - applied F.B. voltage.



(w) d.h.bw nötig für nichtlineare EBD

(b) EBD of a F.B. pn junction showing quasi-Fermi-levels for e<sup>-</sup> (E<sub>Fn</sub>) and holes (E<sub>Fp</sub>)

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Now,  $e^-$  concentration in CB is (1)  $n_i \exp\left(\frac{E_{Fn} - E_i}{kT}\right)$  — (2)

$$n = n_i \exp\left(\frac{E_{Fn} - E_i}{kT}\right) \quad \text{From fig(b)}$$

Hole concentration in VB is

$$p = n_i \exp\left(\frac{E_i - E_{Fp}}{kT}\right) \quad \text{From fig(b)}$$

From fig(b), we have

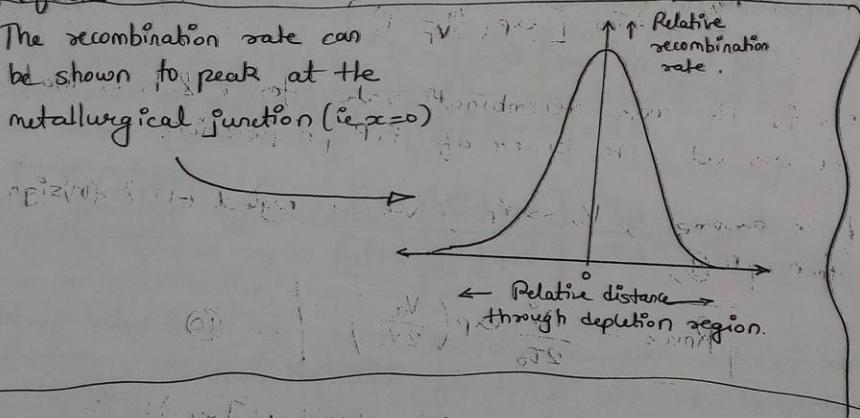
$$(E_{Fn} - E_i) + (E_i - E_{Fp}) = qV_a \quad \text{— (4)}$$

We will assume that the trap level to be at the intrinsic fermi level such that

$$n_t = p_t = n_i \quad \text{— (5)}$$

Reference

The recombination rate can be shown to peak at the metallurgical junction ( $i.e. x=0$ )



At the center of depletion region, we have (From fig(b))

$$E_{Fn} - E_i = E_i - E_{Fp} = \frac{qV_a}{2} \quad \text{— (6)}$$

$\therefore E_{Fn} = E_i + \frac{qV_a}{2}$

Using equation (6) in ② and ③, we get

$$n = n_i \exp\left(\frac{qV_a}{2kT}\right) \quad (7)$$

and

$$p = n_i \exp\left(\frac{qV_a}{2kT}\right) \quad (8)$$

If we assume that  $n_t = p_t = n_i$  and  $T_{no} = T_{po} = T_0$ , then equation ① becomes,

$$R = \frac{n_i^2 \exp\left[\frac{qV_a}{2kT} - n_i^2\right] + (1 - n_i^2)}{2T_0 [n_i \exp\left(\frac{qV_a}{2kT}\right)] + 2T_0 [n_i \exp\left(\frac{qV_a}{2kT}\right)]}$$

$$R_{max} = \frac{n_i}{2T_0} \frac{\left[\exp\left(\frac{V_a}{V_T}\right) - 1\right]}{\left[\exp\left(\frac{V_a}{V_T}\right) - 1\right]} \quad (9) \quad V_T = \frac{kT}{q}$$

$R_{max}$  is the max recombination rate for electrons & holes that occurs at the center of F.B pn junction.

If we assume  $V_a \gg V_T$ , we can neglect (1) & (4) from eqn ⑨,

$$\boxed{R_{max} = \frac{n_i}{2T_0} \exp\left(\frac{V_a}{2V_T}\right)} \quad (10)$$

Now, Recombination current density ( $J_{rec}$ ) is

$$J_{rec} = \int_0^\infty q R dx \quad (11)$$

Integral is over the entire Depletion region.

In this case recombination rate is not constant throughout the depletion region.

$$J_{rec} = q x_m n_i \left[ \frac{V_a}{2T_0} \right] - 12$$

where,  $x_m$  is the width of depletion region where recombination rate is effective.

$$\boxed{J_{rec} = \left\{ \frac{qWn_i}{2T_0} \right\} \exp\left(\frac{V_a}{2V_T}\right)} - 13$$

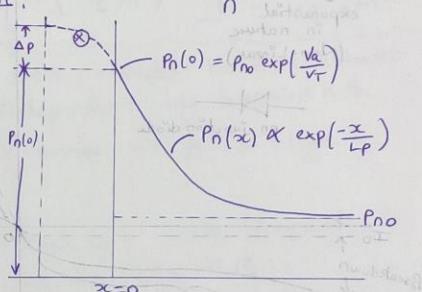
$$J_{rec} = J_{ro} \exp\left(\frac{V_a}{2V_T}\right)$$

→ Total Forward-bias current:

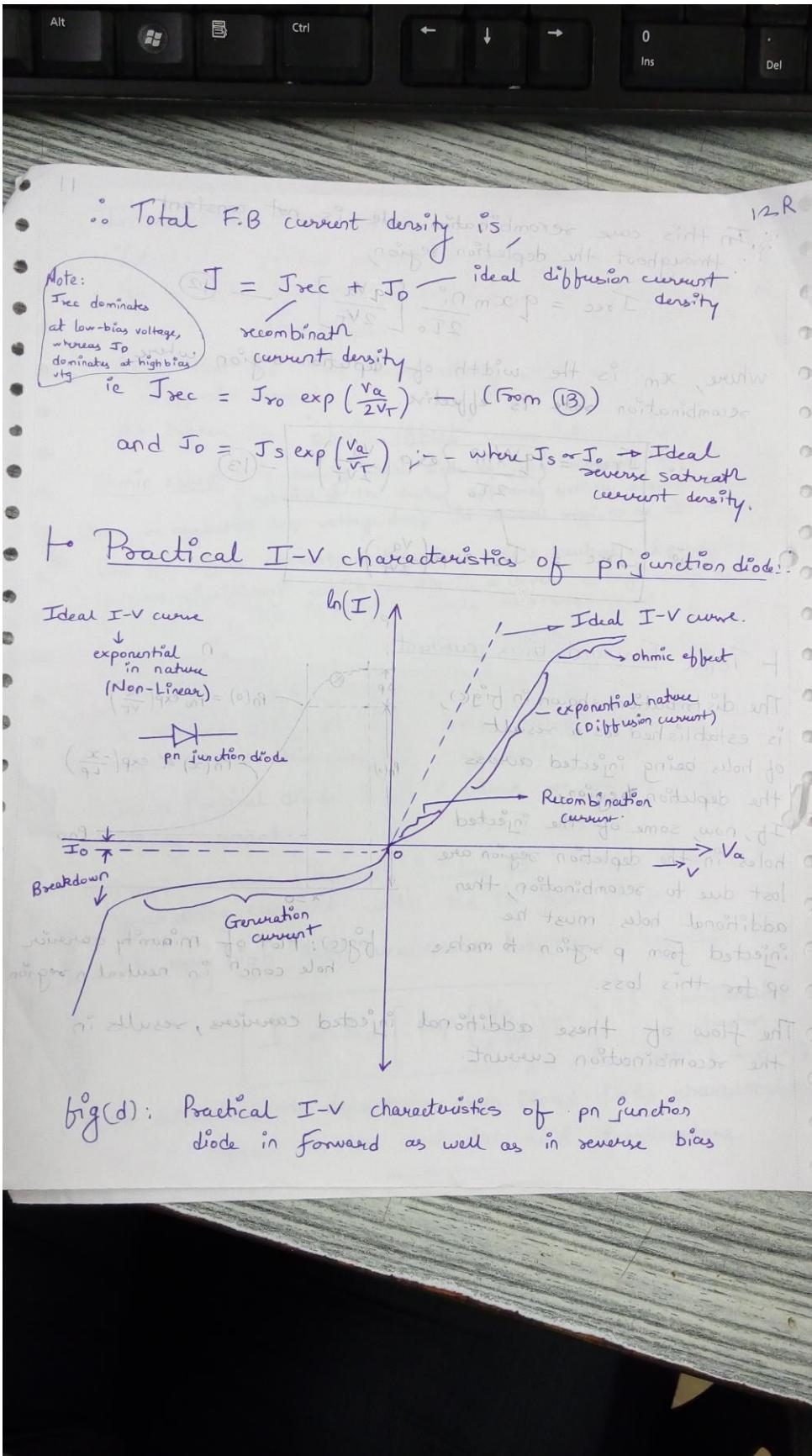
- The distribution shown in fig(c), is established as a result of holes being injected across the depletion region.

If, now, some of the injected holes in the depletion region are lost due to recombination, then additional holes must be injected from p region to make up for this loss.

The flow of these additional injected carriers, results in the recombination current.



fig(c): Plot of minority carrier hole conc' in neutral n-region.



Interpretation from fig(d),

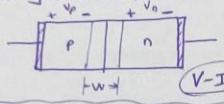
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- Ideal I-V follows exponential nature.
- For low F.B.,  $I_D$  reduces from its exponential value due to recombination in a practical I-V curve  
(diode current)
- After that  $I_D$  increases exponentially with voltage due to diffusion current.
- At higher  $I_D$ , "ohmic effect" comes into picture.

Ohmic effect: { In deriving ideal diode eqn, we assumed that voltage applied to the device appears entirely across the junctions}

Thus, we neglected any voltage drop in neutral regions or at external contacts

- But for higher current, voltage drop across neutral n & p regions (ie  $V_{PLV}$ ) becomes significant which results in a lower voltage drop across pn junction. This is called 'Series-resistance effect'



$$I = I_0 \left[ \exp \left( \frac{V - IR_s}{n k T} \right) - 1 \right] \rightarrow \text{Real diode eqn.}$$

- In reverse bias, an ideal diode should have constant reverse saturation current independent of applied R.B.
- In a Practical diode ie in I-V curve we get a slightly rising current.
- Rise in reverse current is due to the increase in generating current with rise in rising R.B.

At very high R.B. avalanche breakdown occurs.

$$I = I_0 \left[ \exp \left( \frac{V_a}{n k T} \right) - 1 \right]$$

$n \rightarrow$  Ideality factor  
(Btw 1 and 2)

$n \rightarrow$  determines the departure from ideal diode characteristics.

$\hookrightarrow$  It depends on the material and temperature.

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