D. J. SANGHVI COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRONICS ENGINEERING
ELECTRONIC DEVICES SEM III
WEEK 1 TEST

N.B. : **[Total Marks: 100]**

1) Answer questions on the question paper **itself**.
2) Write your **SAP ID** on the top of the first page.
3) This test is based on **semiconductor fundamentals** topic.

1. **Important Definitions:** Please write a brief definition of each of the following terms. Please write your answers in the space provided below. **[30]**

Conduction Band:

Valence Band:

Band Gap:

Intrinsic Semiconductor:

Extrinsic Semiconductor:

Majority Carriers:

Minority Carriers:

Fermi Level:

[TURN OVER

n^+ Material:

p^+ Material:

Degenerate Material:

Minority Carrier Lifetime:

Photo-generation:

Equilibrium:

Law of Mass Action:

2. Indicate which of the following statements are true: Tick on the correct options: **[02]**

The conduction band is the highest filled energy band in a semiconductor or an insulator. It is completely full at $0K$.

The Fermi level inside a semiconductor is also the chemical potential of the electrons in the material.

Equilibrium occurs in a system when its properties do not change with time. This could occur in the presence of an applied electric field or other driving force.

Fermi level is the energy level at which 50% of available states are occupied by electrons.

At equilibrium, the product of n_0 and p_0 is a constant.

3. Which of the following statements are true. Tick on the correct options: **[02]**

In order to make intrinsic semiconductors, dopant impurity levels must be kept very low.

Group-III elements can be used as n-type dopants in silicon.

Group-III elements can be used as p-type dopants in silicon.

Group-V elements can be used as p-type dopants in silicon.

Group II, IV, and VI elements can all be suitable dopants for GaAs.

4. How many Joules in an electron volt ? **[01]**

1

9.22×10^{-31}

1.38×10^{-23}

6.63×10^{-34}

1.6×10^{-19}

5. Which of the following is true about the conduction(valence band) ? **[01]**

It is mostly full(empty) of electrons

It is mostly empty(full) of electrons

Both are exactly half full of electrons

Both are mostly empty of electrons

Both are mostly full of electrons

6. Which of the following is true about an intrinsic semiconductor ? **[01]**

Electron concentration n equals hole concentration p

The concentration of electrons is n_i

The concentration of holes is n_i

The concentration of electrons and holes increases with increasing temperature

All of the above

7. Where is a donor level located on an energy band diagram ? **[01]**

Far above E_C

A little below E_C

About midway between E_C and E_V

A little above E_V

Way above E_V

8. Which of the following is true in equilibrium ? **[01]**

    $n = n_i = 1/p$

    $n = n_i$

    $p_i = n_i$

    $np = n_i^2$

    $p = p_i$

9. What is the mathematical statement of space-charge neutrality ? **[01]**

10. Velocity, mobility, and electric field are related by $v = \mu E$. What are the units of mobility ? **[01]**

    cm/s

    cm^2/s

    $cm^2.V/s$

    $cm^2/(V.s)$

    $cm^2.V.s$

11. As the doping of a semiconductor increases, the mobility generally: **[01]**

    Stays the same

    Increases

    Decreases

    First increases, then decreases

    First decreases, then increases

12. The Einstein relation relates what two quantities ? **[01]**

    The diffusion coefficient and the minority carrier lifetime

    The diffusion length and the minority carrier lifetime

    The hole and electron mobilities

    The mobility and the diffusion coefficient

    The hole and electron diffusion coefficient

13. Which of the following is the minority carrier diffusion length ? **[01]**

    $L_n = \sqrt{\mu_n/\tau_n}$

    $L_n = \sqrt{D_n/\tau_n}$

    $L_n = \sqrt{\mu_n \tau_n}$

    $L_n = \sqrt{D_n \tau_n}$

    $L_n = D_n \tau_n$

14. Which of the following describes the parameter τ_n in a p-type semiconductor **[01]**

It is the average time it takes for an electron to diffuse across the region.

It is the average time between scattering events.

It is the average time before a minority carrier electron recombine with a hole.

It is the average time for an electron to drift across the region.

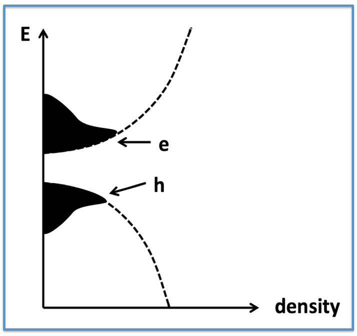
None of the above

15. a) For intrinsic Silicon, what is the intrinsic carrier concentration (in carriers per cm^3) at room temperature? **[02]**

n_i in (cm^3) :

b) How many orders of magnitude lower is the intrinsic carrier concentration in silicon compared to the concentration of atoms in the crystal ? (For example, the number 10 is two orders of magnitude smaller than the number 1000)

16. Identify the following diagrams: Tick the correct option.
Diagram 1: What does the shaded region represent? **[02]**



Density of States for an intrinsic semiconductor

Density of States for an n-type semiconductor

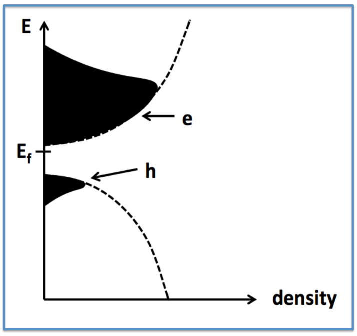
Density of States for an p-type semiconductor

Carrier Distribution in an intrinsic semiconductor

Carrier Distribution in an n-type semiconductor

Carrier Distribution in an p-type semiconductor

17. Identify the following diagrams: Tick the correct option.
Diagram 2: What does the shaded region represent? **[02]**



Density of States for an intrinsic semiconductor

Density of States for an n-type semiconductor

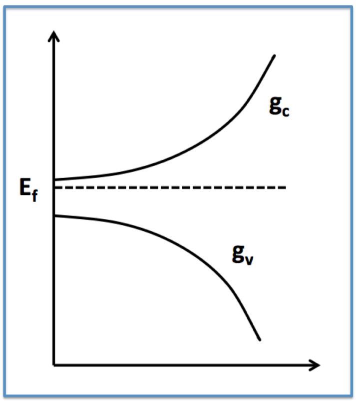
Density of States for an p-type semiconductor

Carrier Distribution in an intrinsic semiconductor

Carrier Distribution in an n-type semiconductor

Carrier Distribution in an p-type semiconductor

18. Identify the following diagrams: Tick the correct option. **[02]**



Density of States for an intrinsic semiconductor

Density of States for an n-type semiconductor

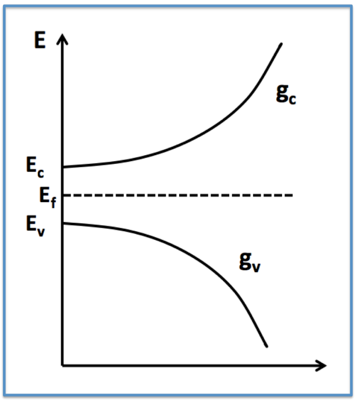
Density of States for an p-type semiconductor

Carrier Distribution in an intrinsic semiconductor

Carrier Distribution in an n-type semiconductor

Carrier Distribution in an p-type semiconductor

19. Identify the following diagrams: Tick the correct option. **[02]**



Density of States for an intrinsic semiconductor

Density of States for an n-type semiconductor

Density of States for an p-type semiconductor

Carrier Distribution in an intrinsic semiconductor

Carrier Distribution in an n-type semiconductor

Carrier Distribution in an p-type semiconductor

20. Assume that a piece of silicon at room temperature is doped with $10^{16}\ cm^{-3}$ phosphorus atoms. What is the hole concentration in the material? **[02]**

p (in cm^3) :

21. Problem on Fermi−Dirac Function: **[03]**

Let's assume that we have an impurity level that is located $0.05eV$ below the Fermi level in silicon. What is the probability that this state is occupied at $300K$? Given: $KT = 0.026meV$

P(occupation):

22. A semiconductor is doped with acceptor impurities with density $N_A = 10^{19}\ cm^{-3}$. The impurity level is at $E_g/5$ above E_v, $E_g = 20kT$, E_i is $10KT$ above E_v and $E_f = 5kT$ above E_v (note that T, the temperature, is not necessarily room temperature).The effective masses for electrons and holes are same. **[10]**

    What fraction of acceptors are ionized ?

    What is n_i ?

    What are the values of n and p ?

23. Problem on Drift Transport

    Assume that you have a piece of silicon doped with $10^{16}/cm^3$ of phosphorus atoms. Assume that you apply an electric field of $5kV/cm$ across the silicon. At this doping level, $\mu_n = 1248cm^2/Vs$ and $\mu_p = 437cm^2/Vs$ in silicon at room temperature. **[06]**

    What is the velocity of the electrons in the silicon under these conditions ?

    v_d (in cm/s) :

    What current density does this correspond to ?

    J (in A/cm^2) :

What is the conductivity of the silicon under the conditions described above ?

σ (in S/cm) :

24. Problem on Einstein Relation **[02]**

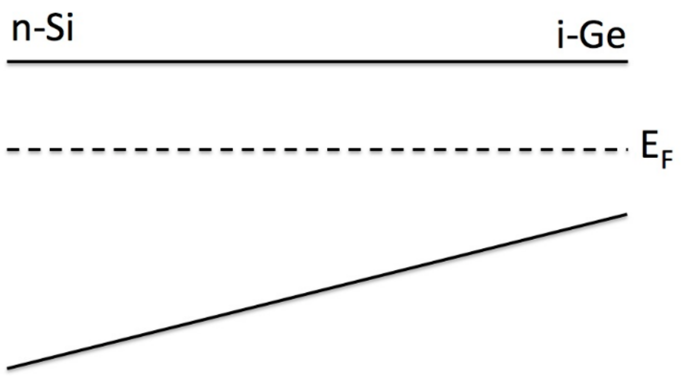
At room temperature, electrons in GaAs have a mobility of about $7000 cm^2/Vs$ at a doping level of $10^{16}/cm^3$. Calculate the diffusion coefficient of electrons in GaAs at this doping concentration at room temperature.

D_n (in cm^2/s) :

25. Find the Fermi potential for a p-type Si semiconductor having dopant concentration $N_A = 10^{16}/cm^3$. Given: $n_i = 10^{10}$, $kT/q = 26mV$: **[02]**

ϕ_{F_p} in (Volts):

26. Consider the band diagram shown below, and then answer the following questions: **[10]**



Is the semiconductor at equilibrium ?

Yes

No

What is the direction of electron diffusion ?

The electrons diffuse from left to right

The electrons diffuse from right to left

There is no net electron diffusion current

What is the direction of electron drift?

The electrons drift from left to right

The electrons drift from right to left

There is no net electron drift current

What is the direction of hole diffusion ?

The holes diffuse from left to right

The holes diffuse from right to left

There is no net hole diffusion current

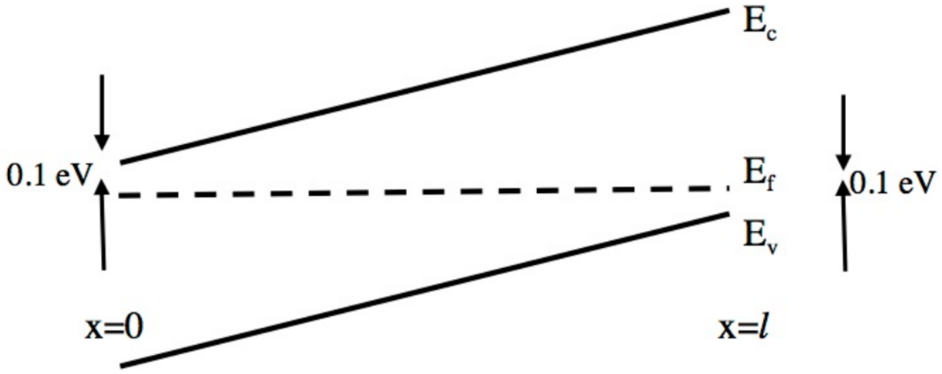
What is the direction of hole drift?

The holes drift from left to right

The holes drift from right to left

There is no net hole drift current

27. Consider a piece of Si with a band structure as shown below. At each end of the sample, the energy difference between the Fermi level and the band edge is $0.1eV$. Take $E_g = 1.1eV$ and neglect the mobility and mass difference between electrons and holes. **[10]**



Indicate which of the following statements with regards to the band structure shown are true:

A dopant concentration gradient could cause the bands to behave in the manner shown.

An externally applied electric field could cause the bands to behave in the manner shown.

This material does not have an internal electric field.

A hole would have a lower energy on the right side of the diagram than on the left.

What is the direction of electron diffusion ?

The electrons diffuse from left to right

The electrons diffuse from right to left

There is no net electron diffusion current

What is the direction of electron drift?

The electrons drift from left to right

The electrons drift from right to left

There is no net electron drift current

What is the direction of hole diffusion ?

The holes diffuse from left to right

The holes diffuse from right to left

There is no net hole diffusion current

What is the direction of hole drift?

The holes drift from left to right

The holes drift from right to left

There is no net hole drift current

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