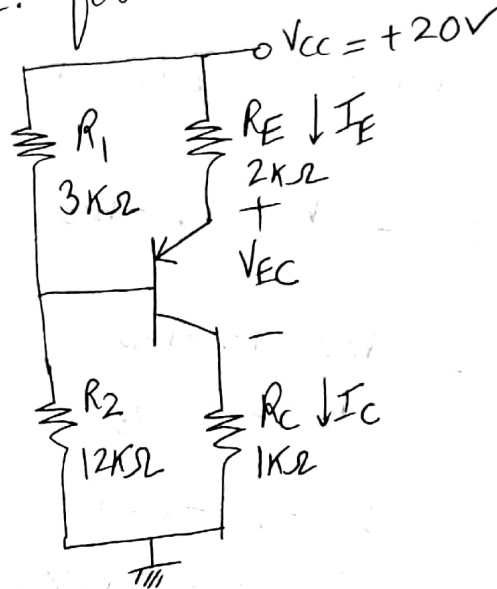


Numerical 01:-

PNP transistor

01
27/11/19

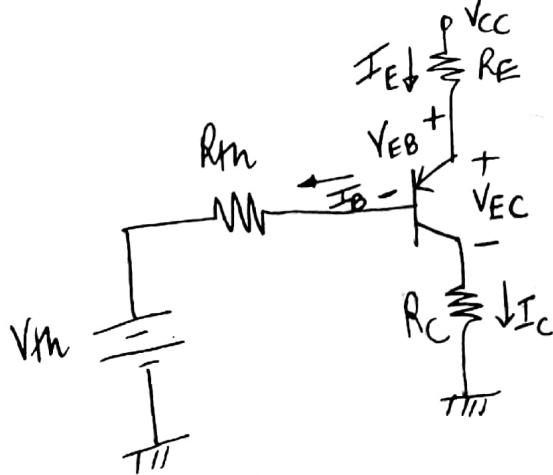
Determine quiescent collector current and emitter-collector voltage. for circuit below :-



Given
 $V_{EB(ON)} = 0.7V$
 $\beta = 100$

Solⁿ:- For pnp transistor as compared to npn transistor have all terminal currents direction & voltage polarities are reversed.
 (I_C, I_B, I_E) (V_{EC}, V_{EB})

Applying ^{CRT} thevenin's equivalent at base, theorem & drawing



$$V_{th} = \frac{R_2}{R_1 + R_2} V_{cc} = \frac{12k}{3k + 12k} \times 20$$

$$V_{th} = 16V$$

$$R_{th} = R_1 \parallel R_2 = 3k \parallel 12k$$

$$R_{th} = 2.4k\Omega$$

KVL to B-E loop gives,

$$V_{th} + I_B R_{th} + V_{EB} + I_E R_E - V_{CC} = 0$$

$$V_{th} + I_B R_{th} + V_{EB} + (1+\beta)I_B R_E - V_{CC} = 0$$

$$I_E = (1+\beta)I_B$$

$$I_B (R_{th} + (1+\beta)R_E) = V_{CC} - V_{th} - V_{EB}$$

$$I_{BQ} = \frac{V_{CC} - V_{th} - V_{EB}}{R_{th} + (1+\beta)R_E}$$

$$= \frac{20 - 16 - 0.7}{2.4K\Omega + 101 \times 2K\Omega}$$

$$I_{BQ} = 16.14 \mu A$$

ie $I_{CQ} = \beta I_{BQ} = 100 \times 16.14 \mu A$

$I_{CQ} = 1.61 \text{ mA}$; $I_E = I_C + I_B = 1.63 \text{ mA}$

KVL to C-E loop gives,

$$V_{CC} - I_E R_E - V_{EC} - I_C R_C = 0$$

$$V_{EC} = V_{CC} - I_E R_E - I_C R_C$$

$$= 20 - 1.63 \text{ mA} \times 2K\Omega - 1.61 \text{ mA} \times 1K\Omega$$

$V_{EC} = 15.1 \text{ V}$